

QSFP28 50GE LR 10 km (OM9380LS100)

# Databrief

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## Product Features

- QSFP28 MSA compliant
- Hot pluggable 38 pin electrical interface
- 1x50G PAM4 transmitter
- 26.5625 Gbps Channel Electrical Serial Interface (50GAUI-2)
- Maximum power consumption 3.5 W
- LC duplex connector
- Supports 53.125 Gbps aggregate bit rate
- Up to 10 km transmission on single mode fiber
- Operating case temperature: 0°C to 70°C
- Single 3.3 V power supply
- RoHS 2 compliant

## Applications

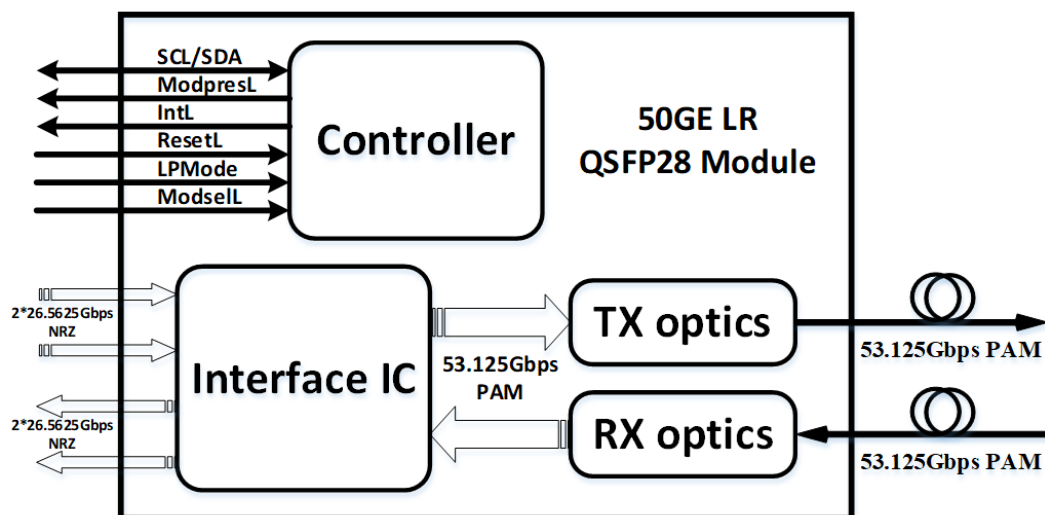
- 50GBASE-LR
- Telecom Networking



## 1. Function Description

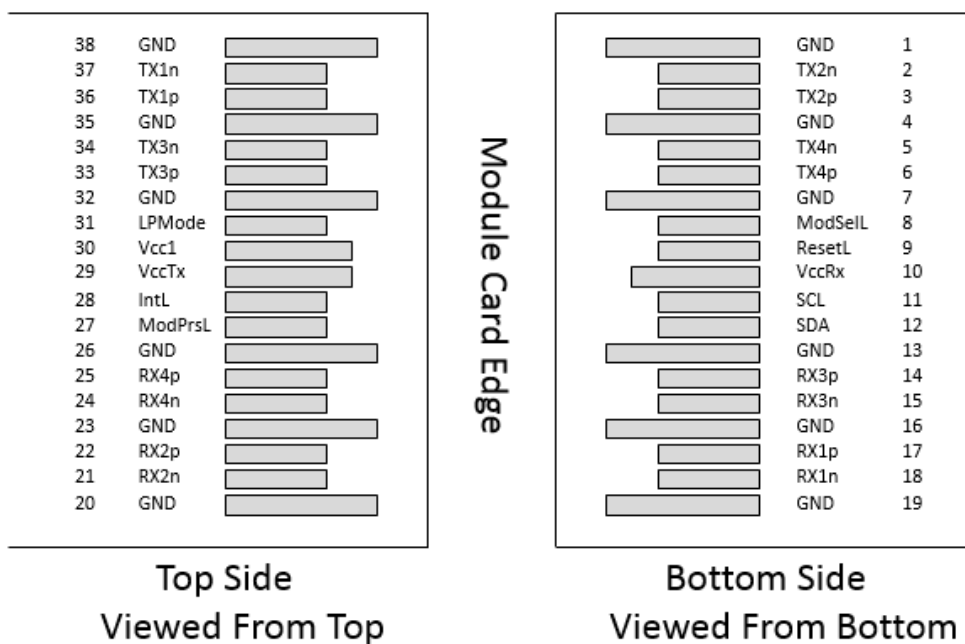
Hisilicon's OM9380LS100 QSFP28 transceiver module is designed for use in 50 Gigabit Ethernet links on up to 10km of single mode fiber. It is compliant with the QSFP28 MSA (SFF-8679 SFF-8636, etc), IEEE P802.3cn. Digital diagnostic functions are available via the I2C interface, as specified by the MSA. A block diagram is shown in Figure 1-1.

Figure 1-1 Transceiver block diagram



## 2. Pin Descriptions

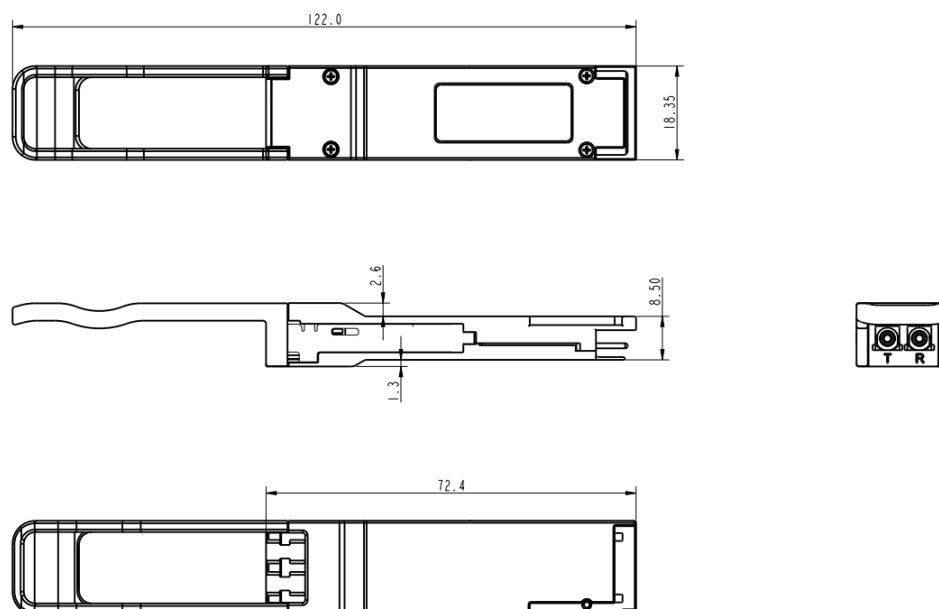
Figure 2-1 MSA compliant connector



### 3. Mechanical Specifications

OM9380LS100 50GE LR QSFP28 transceivers are compatible with the QSFP28 Specification for pluggable form factor modules.

Figure 3-1 Mechanical dimensions




### 4. Ordering Information

Part Number	Description
OM9380LS100	50G BASE-LR QSFP28, Pull-tap

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